



## PATENT APPLICATION

**RESPONSE UNDER 37 CFR §1.116 EXPEDITED PROCEDURE TECHNOLOGY CENTER ART UNIT 2825** 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akira SATO

Group Art Unit: 2825

Application No.:

09/424,500

Examiner:

G. LEE

Filed: February 22, 2000

Docket No.:

104788

For:

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, MOLDING DEVICE FOR SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR DEVICE

## **REQUEST FOP RECONSIDERATION** AFTER FINAL REJECTION UNDER 37 CFR §1.116

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

In reply to the Office Action mailed November 19, 2002, reconsideration of identified application is respectfully requested

Claims 1-9, 11-18, 20-22, 24-27, 29 and 30 are pending.

Applicant appreciates the Office Action's indication. above-identified application is respectfully requested

Applicant appreciates the Office Action's indication that claims 11 and 24 contain allowable subject matter.

The Office Action rejects claims 1-7, 15-17, 20 and 21 under 35 U.S.C. §102(b) over JP 09076282, claims 25 and 26 under 35 U.S.C. §103(a) over JP '282 and claims 8, 9, 12, 14, 18, 22, 27 and 30 under 35 U.S.C. §103(a) over JP '282 in view of JP 06177268. These rejections are respectfully traversed.

JP '282 does not disclose applying a pressure to the semiconductor assembly by at least one support pin so as to cause a stress in the lead frame as recited in independent claim

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1, applying a pressure to the heat radiator by at least one support pin so as to cause a stress in

the lead frame as recited in independent claim 12 and applying a pressure to the

semiconductor assembly with the support pin so as to cause a stress in the lead frame as in the

invention of claim 15.

Instead, JP '282 discloses a substrate 2 that is sandwiched between pins 10 and 11.

Thus, no stress is caused in the substrate 2.

In view of the foregoing, Applicant respectfully submits that this application is in

condition for allowance. Favorable consideration and prompt allowance are earnestly

solicited.

Should the Examiner believe that anything further would be desirable in order to

place the application in even better condition for allowance, the Examiner is invited to

contact Applicant's undersigned representative at the telephone number listed below.

Respectfully submitted,

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Date: February 19, 2003

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